

DEC 29 2004

## Patent Agent

429 26th Ave., Santa Cruz, CA 95062

## FAX

Date: 12/29/04Number of pages including cover: 15To: ExaminerFax phone: 703-872-9306Org: GAU 1765 US PTOFrom: Stuart AuvinenPhone: (831) 476-5506Fax phone: (831) 477-0703REMARKS: ☐ Urgent ☐ For your review ☐ Reply ASAP ☐ Please comment

Ser. No. 10/711,869 Group 1765 Docket PS-117

## SECOND IDS

Attached is the second IDS with non-patent references attached.

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PATENT  
PS-117

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of	)	
Taylor et al.	)	Examiner:
Serial No. 10/711,869	)	Group Art Unit: 1765
Filed: 10/11/2004	)	
For: A Printed-Circuit-Board (PCB) Module with a Milled-Back Insulator Layer around Flat Metal Pads for Surface-Mounting to a Larger Board	)	

SECOND INFORMATION DISCLOSURE STATEMENT

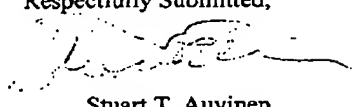
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Sir:

Attached is a completed Substitute Form PTO-1449 and copies of the pertinent parts of the references cited thereon.

- ☐ The commissioner is hereby authorized to charge payment of any patent application processing fees under 37 C.F.R. § 1.17 associated with this communication or credit any overpayment to deposit account No. 01-2950.

Respectfully Submitted,

  
Stuart T. Auvinen  
Agent for Applicant  
Reg. No. 36,435

Sheet 1 of 1

<b>Form PTO-1449</b>				<b>Docket Number (Optional)</b> PS-117		<b>Application Number</b> 10/711,869	
<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b> <i>(Use several sheets if necessary)</i>				<b>Applicant</b> Taylor et al.			
				<b>Filing Date</b> 10/11/2004		<b>Group Art Unit</b> 1765	
<b>U.S. PATENT DOCUMENTS</b>							
<b>EXAMINER INITIAL</b>	<b>DOCUMENT NUMBER</b>			<b>DATE</b>	<b>NAME</b>	<b>CLASS</b>	<b>SUBCLASS</b>
<b>FOREIGN PATENT DOCUMENTS</b>							
	<b>DOCUMENT NUMBER</b>			<b>DATE</b>	<b>NAME</b>	<b>CLASS</b>	<b>SUBCLASS</b>
<b>OTHER DOCUMENTS</b> <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>							
	J-lead Clip Drawing, "Surface Mount Applications" . June 9, 2003						
	( from <a href="http://www.nasinterplex.com/nas_smt/smt61.html">http://www.nasinterplex.com/nas_smt/smt61.html</a> )						
	SCG2500 Synchronous Clock Generators, Prelim. Data Sheet # SG027, Connor Winfield, pp 1-10, 3/15/02						
<b>EXAMINER</b>				<b>DATE CONSIDERED</b>			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.							

Substitute PTO/SB/08 (10-92)

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